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MorethanIP Announces 40/100-GbE IP Cores For Altera's Stratix IV FPGAs and HardCopy IV ASICs

Karlsfeld, Germany, March 2nd, 2009—MorethanIP today announced the immediate availability of 40/100-Gigabit Ethernet (GbE) intellectual property (IP) cores supporting Altera's 40-nm Stratix® IV GT and Stratix IV GX FPGAs and HardCopy® IV GX ASICs. Supporting both 10.3-Gbps and 5.15-Gbps serial transceivers, MorethanIP's 40/100-GbE IP core is easily implemented in these devices, enabling the development of next-generation enterprise wireline, storage, telecommunications and broadcast applications. The IP cores are designed to be easily retargeted to standard cell ASICs by automatically eliminating pipeline stages and optimizing for size and latency.

Currently shipping, Stratix IV GT FPGAs and Stratix IV GX FPGAs feature serial transceivers with data rates of 11.3 Gbps and 8.5 Gbps, respectively. Stratix IV GT devices are the first and only FPGAs with integrated 11.3 Gbps serial transceivers. The PCS core interfaces the Stratix IV FPGA's embedded transceivers with a 40-bit interface and includes a complete MLD function to provide a single chip solution. The PCS core is designed for very low latency while providing the possibility to use a single clock for the MAC and PCS transmit and receive functions.

To support emerging standards and applications such as IEEE 1588 and FCoE (Fiber Channel over Ethernet), the MAC implements a TSU (Time Stamping Unit) and per-channel flow control. The MAC and PCS cores are compliant with the latest IEEE 802.3ba Draft 2.0 and MorethanIP is committed to enhance and adapt the MAC and PCS core to follow the evolution of the IEEE 802.3ba standard to ensure third-party interoperability and full standard compliance.

Francois Balay CEO of MorethanIP stated “We are proud to immediately provide 40/100 GbE MAC and PCS cores for Stratix IV FPGAs and HardCopy IV ASICs and have the cores available prior to the standard being completed. Once the standard is

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completed, we will provide a free upgrade. This methodology allows customers to start their designs now and gain a strategic time to market advantage over the competition.”

David Greenfield, Altera's senior director of product marketing, high-end products, added, "MorethanIP is a strategic IP provider for Altera and has a proven track record of supporting our devices with high-quality multi-gigabit Ethernet IP. Our Stratix IV FPGAs and HardCopy IV ASICs provide an ideal solution for meeting the high-speed data rates and bandwidth requirements of 40/100-GbE applications. With MorethanIP's 40/100 GbE IP cores, Altera provides a comprehensive solution to customers targeting this emerging standard."

About MorethanIP's 40/100-GbE IP Cores

The new IP cores are delivered with the MorethanIP user-friendly configuration graphical user interface, through which designers can easily eliminate features that are not required, reducing unit costs and simplifying the software integration. MorethanIP supplies the 40/100-GbE cores with the ability to perform functional simulation using an industry standard VHDL or Verilog simulator or synthesis tools.

About MorethanIP

MorethanIP was founded 1999 as a GmbH (Ltd.) company in Munich Germany and is an IP and design house concentrating on high-speed communications and embedded systems. This includes design specification and implementation of standard products, system architectures and customer specific solutions. For more information on the company and its products, please email info@morethanip.com or visit the company's website at www.morethanip.com.